

ENGINEIQ v5

Proprietary AI for Engineering Assessment

The first ML platform that scores PCB layouts, schematics, CAD geometry, and simulation outputs with theoretical confidence bounds — built for technical hiring, certification, and upskilling.

Spearman $r = 0.942$	49 API Routes	192-dim Fusion	SRHN PAC-Bayes
ML Accuracy	Production	Multi-modal	Theoretically grounded

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GitHub: github.com/engineiq-ai | Seed Round 2025

THE PROBLEM

Engineering hiring is broken

\$47K

Average cost of a bad engineering hire



~68%	Of tech interviews test algorithms, not engineering	■
0	Existing tools that evaluate real PCB / CAD work	■
Manual	All EE assessment today is done by senior engineers	■

THE SOLUTION

EngineIQ — ML scoring for real engineering work

Upload a .kicad_pcb file. In 250ms get: 6 competency scores with theoretical confidence bounds, specific fault locations with IPC-2221 standard citations, causal reasoning chains, and a defensible, hard-to-replicate assessment.

System	What it does	Unique
GBM Scorer	6-competency ML scoring on real EE features	$r=0.942$ Spearman, deterministic
SRHN v4	C11 hypergraph reasoning engine	PAC-Bayes theoretical bounds [R7]
Fault Localizer	17 IPC-2221 rule types, component-level	Specific fix + part number
Behavior Bonus	Interaction trace → anti-gaming signal	+3pts if calc used before submit
GATv2 GNN	Graph-native circuit understanding	MCM + LinkPred pretraining (GPU)

MARKET OPPORTUNITY

\$180B+ total addressable market

TAM	\$180B	Global technical hiring & assessment market
SAM	\$12B	Electronics / embedded / PCB engineering segment
SOM	\$400M	EE assessment platforms — currently no AI-native player
Beachhead	\$45M	US/EU hardware startups hiring EE talent (2025–2027)

TRACTION & ASK

Current state + what we need

✓ Working ML model	Spearman $r=0.942$, 6 competencies, 51ms inference
✓ SRHN v4 integrated	PAC-Bayes bounds, causal chains, C11 binary
✓ 49 production endpoints	Full REST API, Docker Compose, CI/CD ready
✓ 20-page React SPA	Interaction tracer, feedback loop, eval dashboard
■ Seeking: Seed funding	\$750K for 12 months — GPU time + 3 EE annotators + go-to-market
■ Seeking: EE collaborators	Senior PCB/analog/power engineers to annotate 200+ designs
■ Seeking: Enterprise pilots	Technical hiring teams, bootcamps, engineering cert providers

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